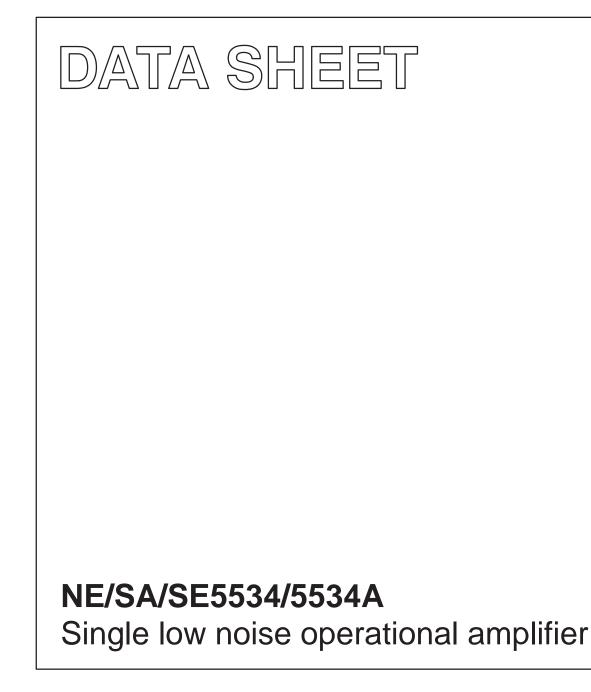
INTEGRATED CIRCUITS



Product data Supersedes data of 1994 Aug 31 File under Integrated Circuits, IC11 Data Handbook 2001 Aug 03



NE/SA/SE5534/5534A

DESCRIPTION

The NE/SA/SE5534/5534A are single high-performance low noise operational amplifiers. Compared to other operational amplifiers, such as TL083, they show better noise performance, improved output drive capability, and considerably higher small-signal and power bandwidths.

This makes the devices especially suitable for application in high quality and professional audio equipment, in instrumentation and control circuits and telephone channel amplifiers. The op amps are internally compensated for gain equal to, or higher than, three. The frequency response can be optimized with an external compensation capacitor for various applications (unity gain amplifier, capacitive load, slew rate, low overshoot, etc.)

FEATURES

- Small-signal bandwidth: 10 MHz
- Output drive capability: 600 Ω , 10V_{RMS} at V_S = ±18 V
- Input noise voltage: 4nV/√Hz
- DC voltage gain: 100000
- AC voltage gain: 6000 at 10 kHz
- Power bandwidth: 200 kHz
- Slew rate: 13 V/μs
- Large supply voltage range: ±3 to ±20 V

PIN CONFIGURATIONS

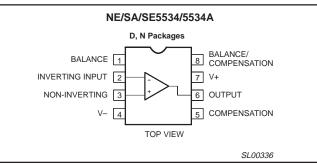


Figure 1. Pin Configuration

APPLICATIONS

- Audio equipment
- Instrumentation and control circuits
- Telephone channel amplifiers
- Medical equipment

DESCRIPTION **TEMPERATURE RANGE** ORDER CODE DWG # 8-Pin Plastic Small Outline (SO) package 0 °C to +70 °C NE5534D SOT96-1 8-Pin Plastic Dual In-Line Package (DIP) 0 °C to +70 °C SOT97-1 NE5534N 8-Pin Plastic Small Outline (SO) package 0 °C to +70 °C NE5534AD SOT96-1 8-Pin Plastic Dual In-Line Package (DIP) 0 °C to +70 °C NE5534AN SOT97-1 8-Pin Plastic Dual In-Line Package (DIP) –40 °C to +85 °C SA5534N SOT97-1 -40 °C to +85 °C 8-Pin Plastic Small Outline (SO) package SA5534AD SOT96-1 8-Pin Plastic Dual In-Line Package (DIP) -40 °C to +85 °C SA5534AN SOT97-1 8-Pin Plastic Dual In-Line Package (DIP) -55 °C to +125 °C SE5534N SOT97-1 8-Pin Plastic Dual In-Line Package (DIP) -55 °C to +125 °C SE5534AN SOT97-1

ORDERING INFORMATION

NE/SA/SE5534/5534A

ABSOLUTE MAXIMUM RATINGS

SYMBOL	PARAMETER	RATING	UNIT
Vs	Supply voltage	±22	V
V _{IN}	Input voltage	±V supply	V
V _{DIFF}	Differential input voltage ¹	±0.5	V
T _{amb}	Operating temperature range NE SA SE	0 to +70 -40 to +85 -55 to +125	⊃° ⊃° ⊃°
T _{stg}	Storage temperature range	-65 to +150	°C
Tj	Junction temperature	150	°C
P _D	Power dissipation at 25 °C ² SO8 package DIP8 package	750 1150	mW mW
	Output short-circuit duration ³	Indefinite	
T _{sld}	Lead soldering temperature (10 sec max)	230	°C

NOTES:

1. Diodes protect the inputs against over voltage. Therefore, unless current-limiting resistors are used, large currents will flow if the differential input voltage exceeds 0.6 V. Maximum current should be limited to ±10 mA.
2. For operation at elevated temperature, derate packages based on the following junction-to-ambient thermal resistance:

8-pin plastic DIP 105 °C/W

8-pin plastic SO 160 °C/W

3. Output may be shorted to ground at V_S = ±15 V, T_{amb} = 25 °C. Temperature and/or supply voltages must be limited to ensure dissipation rating is not exceeded.

DC ELECTRICAL CHARACTERISTICS

 T_{amb} = 25 °C; V_S = ± 15 V, unless otherwise specified. ^{1, 2, 3}

CYMDOL	DADAMETED	TEST CONDITIONS	NE/S	A5534/5	534A	SE	UNIT			
SYMBOL	PARAMETER	TEST CONDITIONS	Min	Тур	Max	Min	Тур	Max		
V _{OS} ΔV _{OS} /ΔT	Offset voltage	Over temperature		0.5 5	4 5		0.5 5	2 3	mV mV μV/°C	
I _{OS} ΔI _{OS} /ΔT	Offset current	Over temperature		20 200	300 400		10 200	200 500	nA nA pA/°C	
Ι _Β ΔΙ _Β /ΔΤ	Input current	Over temperature		500 5	1500 2000		400 5	800 1500	nA nA nA/°C	
I _{CC}	Supply current per op amp	Over temperature		4	8 10		4	6.5 9	mA mA	
V _{CM} CMRR PSRR	Common mode input range Common mode rejection ratio Power supply rejection ratio		±12 70	±13 100 10	100	±12 80	±13 100 10	50	V dΒ μV/V	
A _{VOL}	Large-signal voltage gain	$R_L \ge 600 \Omega$, $V_O = \pm 10 V$ Over temperature	25 15	100		50 25	100		V/mV V/mV	
Vout	Output swing	$ \begin{array}{l} R_L \geq 600 \; \Omega \\ Over \ temperature \\ R_L \geq 600 \; \Omega; \; V_S = \pm 18 \; V \\ R_L \geq 2 \; k\Omega \\ Over \ temperature \end{array} $	$ \begin{array}{c} \pm 12 \\ \pm 10 \\ \pm 15 \\ \pm 13 \\ \pm 12 \end{array} $	± 13 ± 12 ± 16 ± 13.5 ± 12.5		±12 ±10 ±15 ±13 ±12	± 13 ± 12 ± 16 ± 13.5 ± 12.5		V V V V V	
R _{IN}	Input resistance		30	100		50	100		kΩ	
I _{SC}	Output short circuit current			38			38		mA	

NOTES:

1. For NE5534/5534A, $T_{MIN} = 0 \ ^{\circ}C$, $T_{MAX} = 70 \ ^{\circ}C$ 2. For SA5534/5534A, $T_{MIN} = -40 \ ^{\circ}C$, $T_{MAX} = +85 \ ^{\circ}C$ 3. For SE5534/5534A, $T_{MIN} = -55 \ ^{\circ}C$, $T_{MAX} = +125 \ ^{\circ}C$

AC ELECTRICAL CHARACTERISTICS

 T_{amb} = 25 °C, V_S = ± 15 V, unless otherwise specified.

0)///201			NE/S	A5534/5	534A	SE			
SYMBOL	PARAMETER	TEST CONDITIONS	Min	Тур	Max	Min	Тур	Max	UNIT
R _{OUT}	Output resistance	$\begin{array}{l} A_{V} = 30 \text{ dB closed-loop} \\ f = 10 \text{ kHz}; \ R_{L} = 600 \ \Omega; \\ C_{C} = 22 \text{ pF} \end{array}$		0.3			0.3		Ω
	Transient response	$\label{eq:Voltage-follower, V_{IN} = 50 mV} \begin{array}{l} R_{L} = 600 \ \Omega; \ C_{C} = 22 \ pF, \\ C_{L} = 100 \ pF \end{array}$							
t _R	Rise time			20			20		ns
	Overshoot			20			20		%
	Transient response	$V_{IN} = 50 \text{ mV}, \text{ R}_{L} = 600 \Omega$ C _C = 47 pF, C _L = 500 pF							
t _R	Rise time			50			50		ns
	Overshoot			35			35		%
A _V	Gain	$f = 10 \text{ kHz}, C_C = 0$ $f = 10 \text{ kHz}, C_C = 22 \text{ pF}$		6 2.2			6 2.2		V/mV V/mV
GBW	Gain bandwidth product	C _C = 22 pF, C _L = 100 pF		10			10		MHz
SR	Slew rate	C _C = 0 C _C = 22 pF		13 6			13 6		V/μs V/μs
	Power bandwidth	$V_{OUT} = \pm 10 \text{ V}, \text{ C}_{\text{C}} = 0 \text{ pF}$ $V_{OUT} = \pm 10 \text{ V}, \text{ C}_{\text{C}} = 22 \text{ pF}$ $V_{OUT} = \pm 14 \text{ V}, \text{ R}_{\text{L}} = 600 \Omega$ $\text{ C}_{\text{C}} = 22 \text{ pF}, \text{ V}_{\text{CC}} = \pm 18 \text{ V}$		200 95 70			200 95 70		kHz kHz kHz

ELECTRICAL CHARACTERISTICS

 T_{amb} = 25 °C, V_S = 15 V, unless otherwise specified.

SYMBOL	PARAMETER	TEST CONDITIONS	NE/	SA/SE5	534	NE/S	UNIT		
STWIDUL	FARAMETER	TEST CONDITIONS	Min	Тур	Max	Min	Тур	Max	
V _{NOISE}	Input noise voltage	f _O = 30 Hz		7			5.5	7	nV/√Hz
♥ NOISE	input noise voitage	f _O = 1 kHz		4			3.5	4.5	nV/√Hz
	Input noise current	f _O = 30 Hz		2.5			1.5		pA/√Hz
NOISE	input noise current	f _O = 1 kHz		0.6			0.4		pA/√Hz
	Broadband noise figure	f = 10 Hz to 20 kHz; R_S = 5 k Ω					0.9		dB
	Channel separation	f = 1 kHz; $R_S = 5 k\Omega$		110			110		dB

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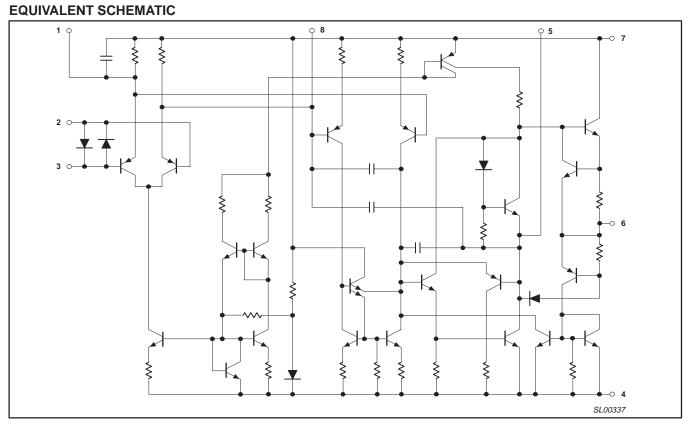


Figure 2. Equivalent Schematic

NE/SA/SE5534/5534A

Single low noise operational amplifier

TYPICAL PERFORMANCE CHARACTERISTICS

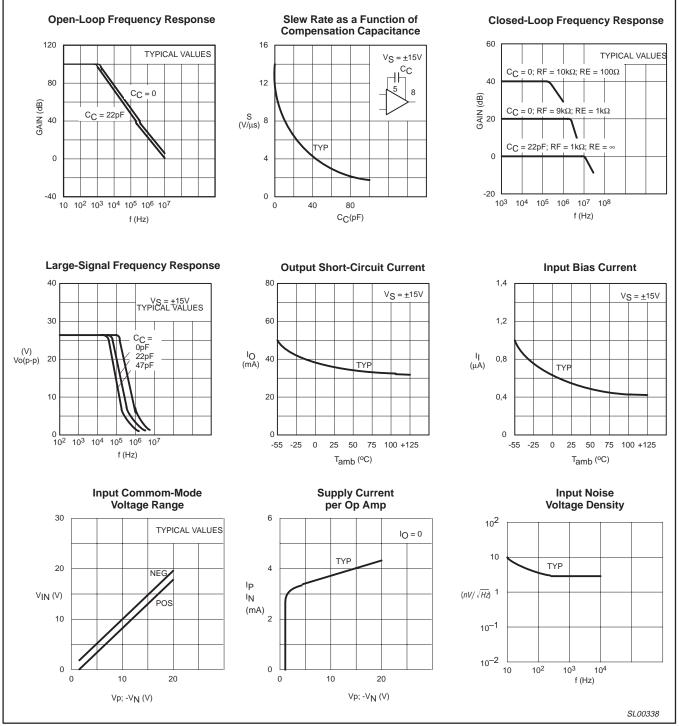


Figure 3. Typical Performance Characteristics

NE/SA/SE5534/5534A



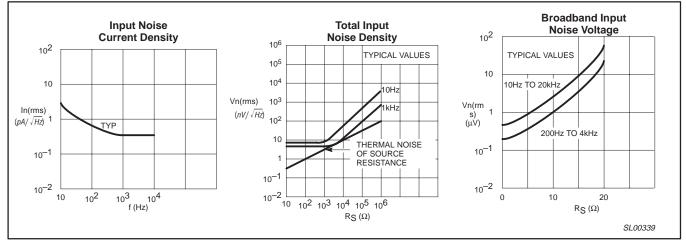


Figure 4. Typical Performance Characteristics (cont.)

TEST LOAD CIRCUITS

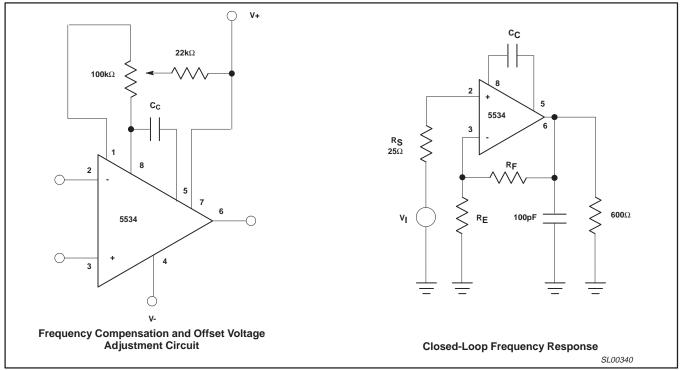


Figure 5. Test Load Circuits

NE/SA/SE5534/5534A

NOISE TEST BLOCK DIAGRAM

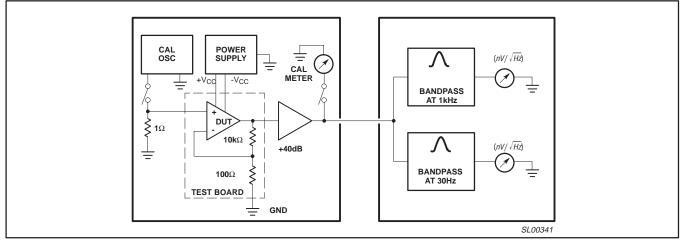
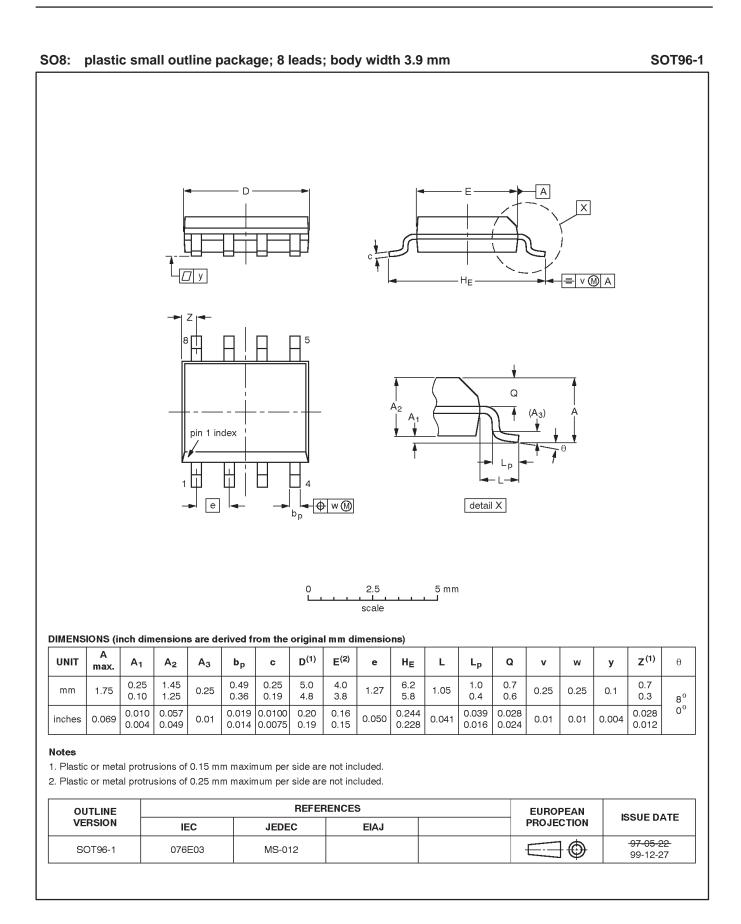
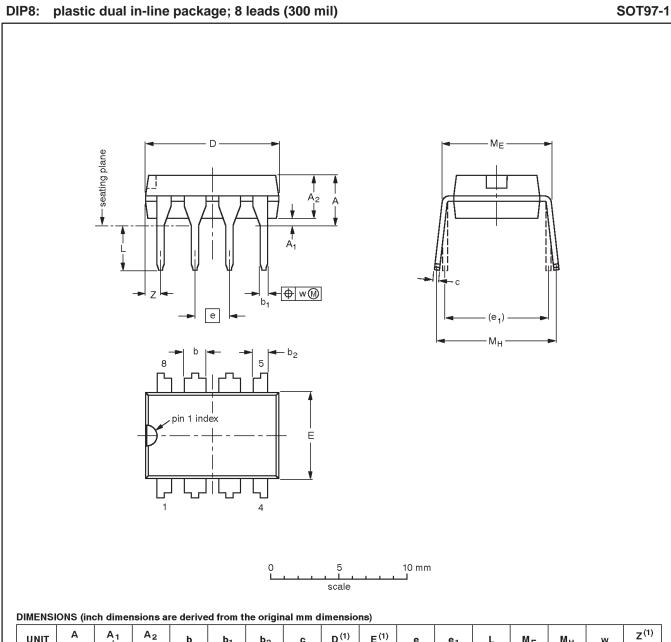


Figure 6. Noise Test Block Diagram

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	UNIT	A max.	A ₁ min.	A ₂ max.	b	b ₁	b ₂	с	D ⁽¹⁾	E ⁽¹⁾	е	e ₁	L	ME	м _н	w	Z ⁽¹⁾ max.
	mm	4.2	0.51	3.2	1.73 1.14	0.53 0.38	1.07 0.89	0.36 0.23	9.8 9.2	6.48 6.20	2.54	7.62	3.60 3.05	8.25 7.80	10.0 8.3	0.254	1.15
i	inches	0.17	0.020	0.13	0.068 0.045	0.021 0.015	0.042 0.035	0.014 0.009	0.39 0.36	0.26 0.24	0.10	0.30	0.14 0.12	0.32 0.31	0.39 0.33	0.01	0.045

Note

1. Plastic or metal protrusions of 0.25 mm maximum per side are not included.

c	UTLINE		REFER	ENCES	EUROPEAN ISSUE DATE			
V	ERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE	
5	SOT97-1	050G01	MO-001	SC-504-8			-95-02-04 99-12-27	

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NOTES

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